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Fig.1

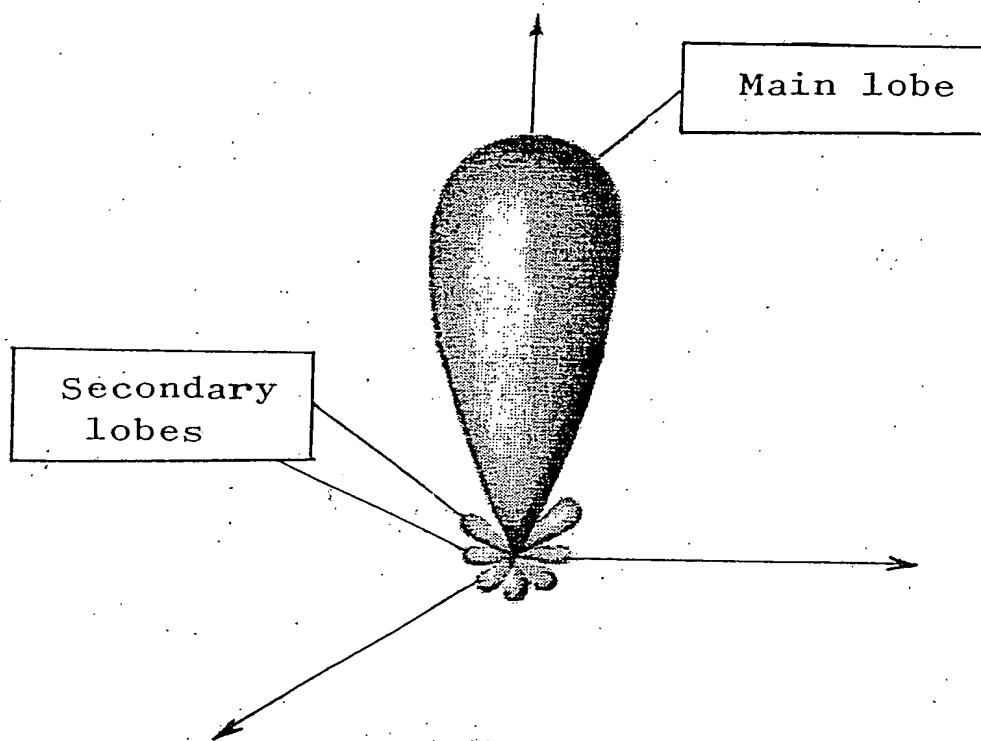
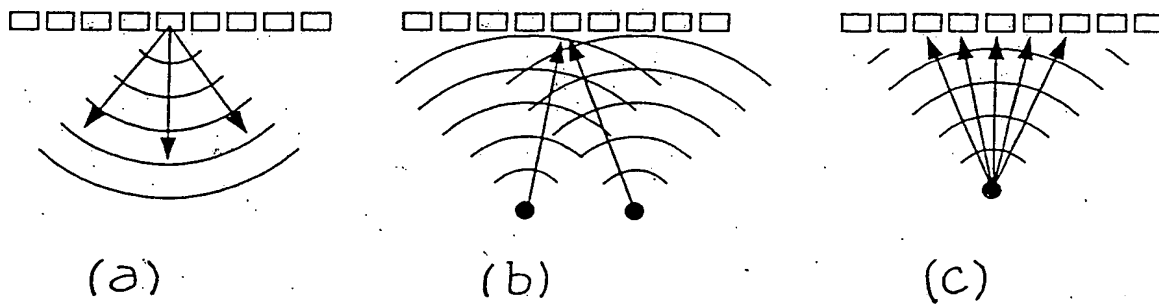


Fig.2



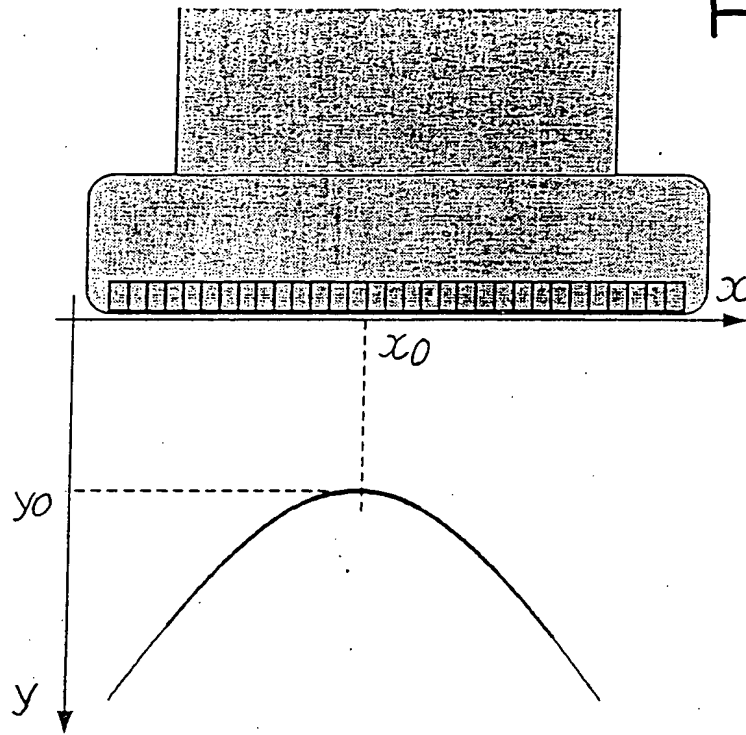


Fig. 3

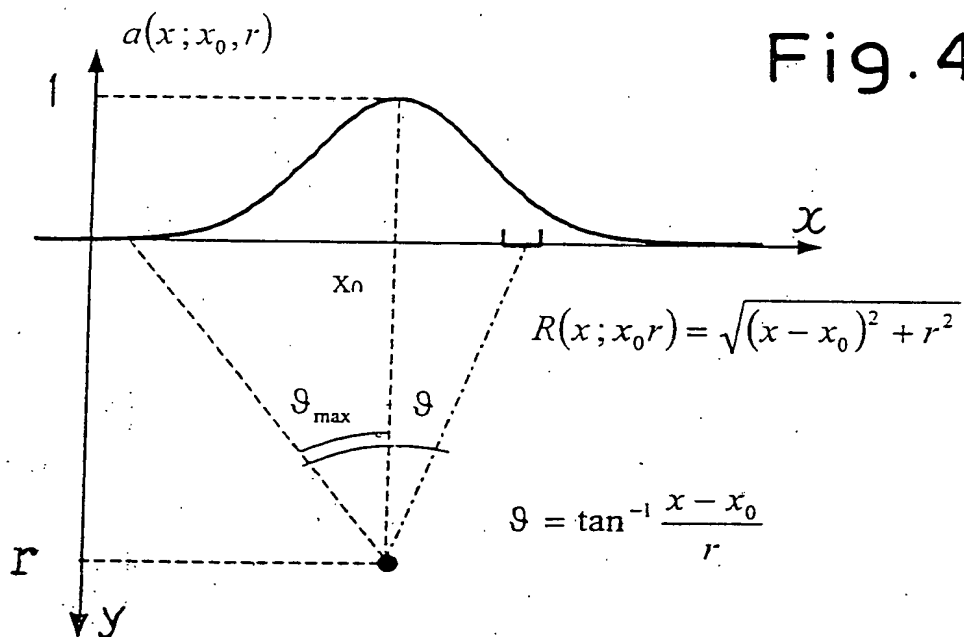


Fig. 4

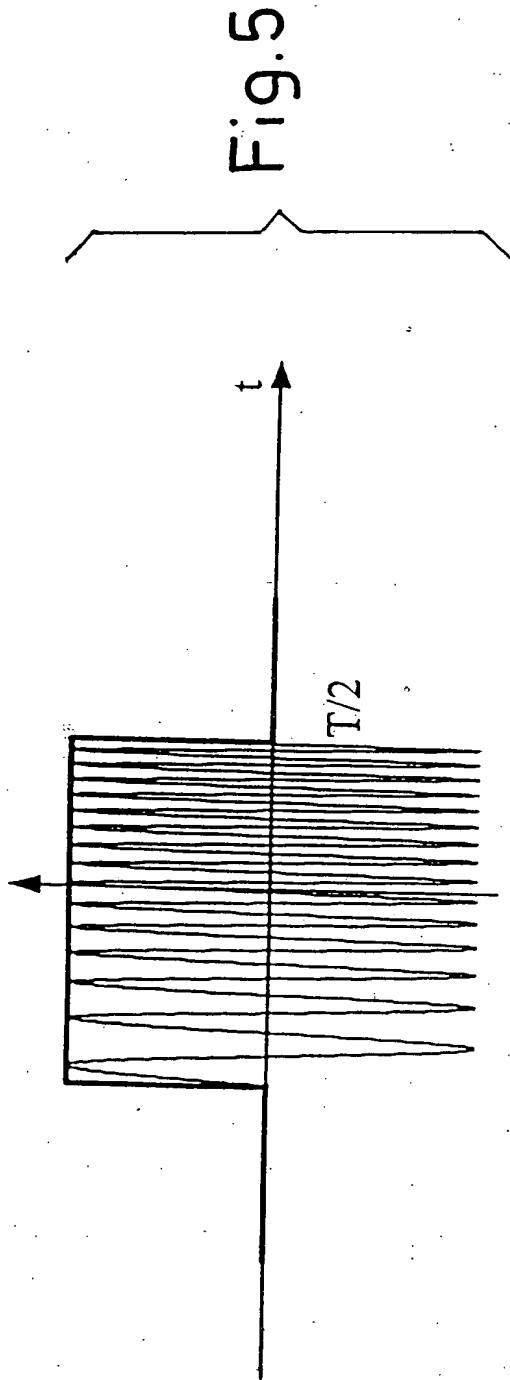
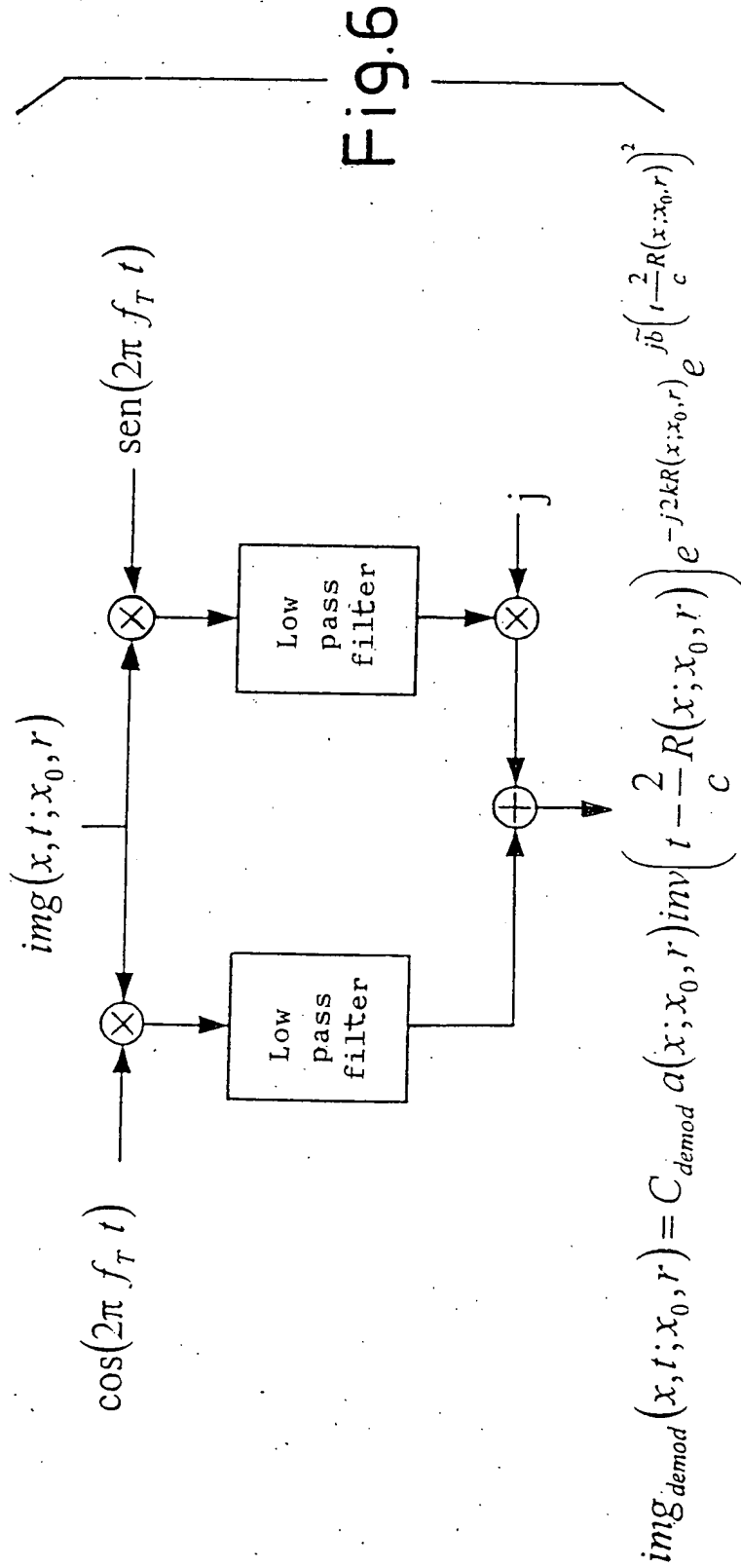


Fig.5



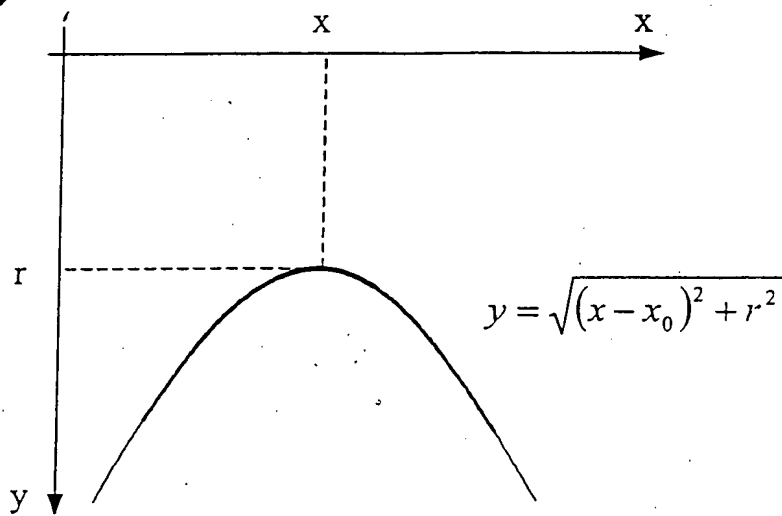


Fig.7

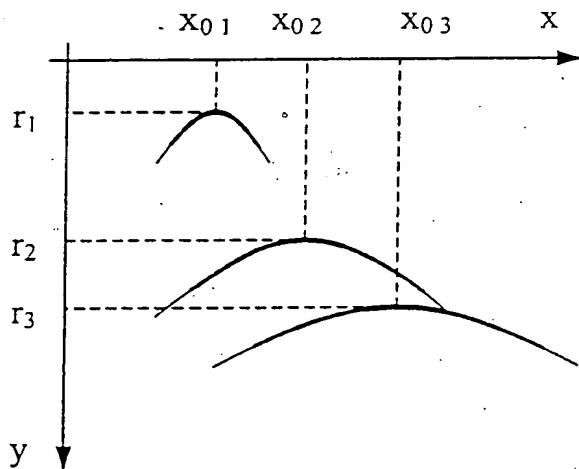


Fig.8

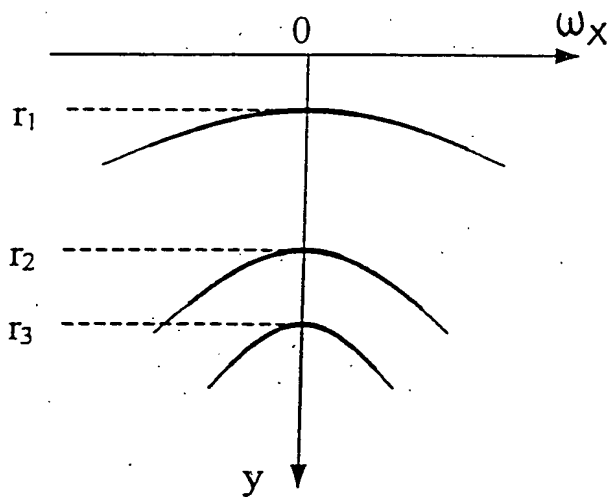


Fig.9



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Fig.10

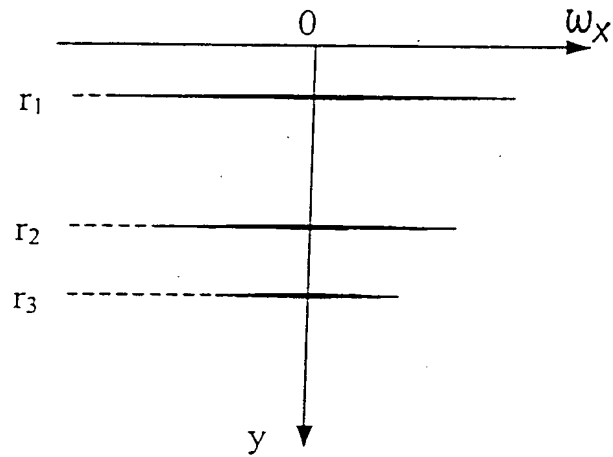
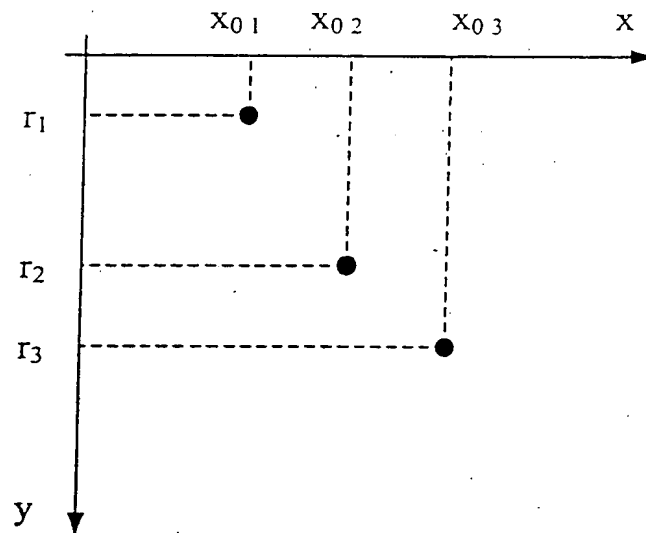


Fig.11





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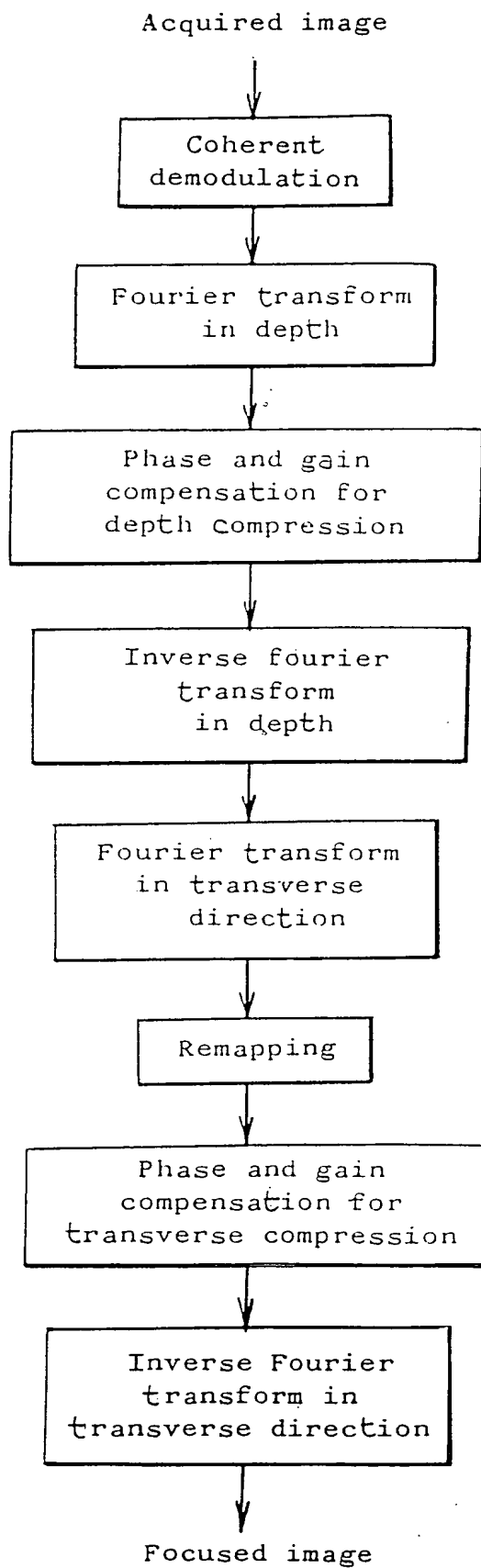


Fig.12



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Fig.13

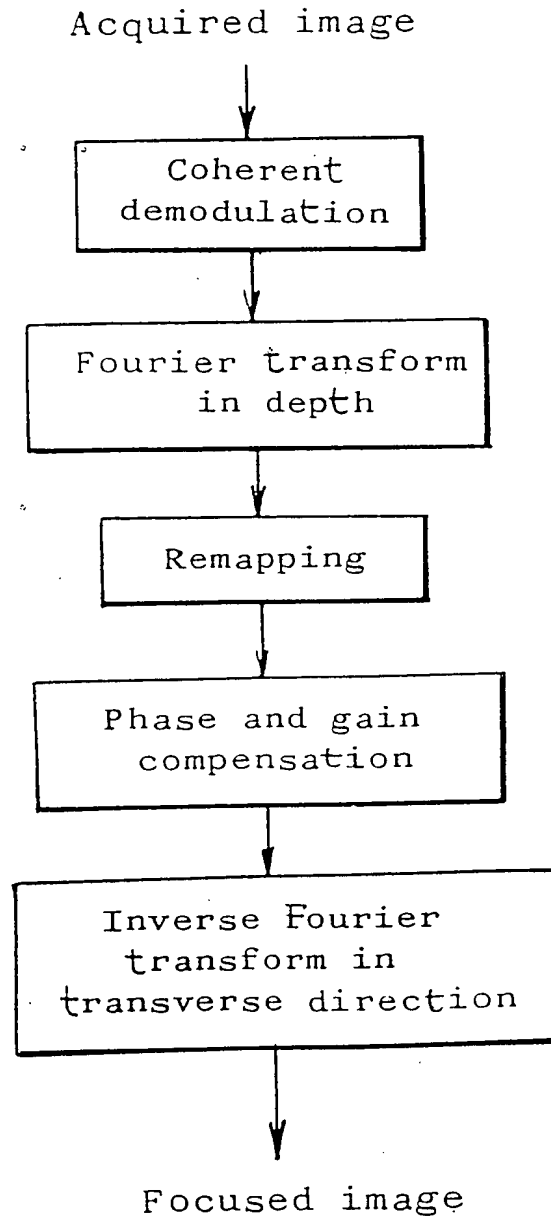
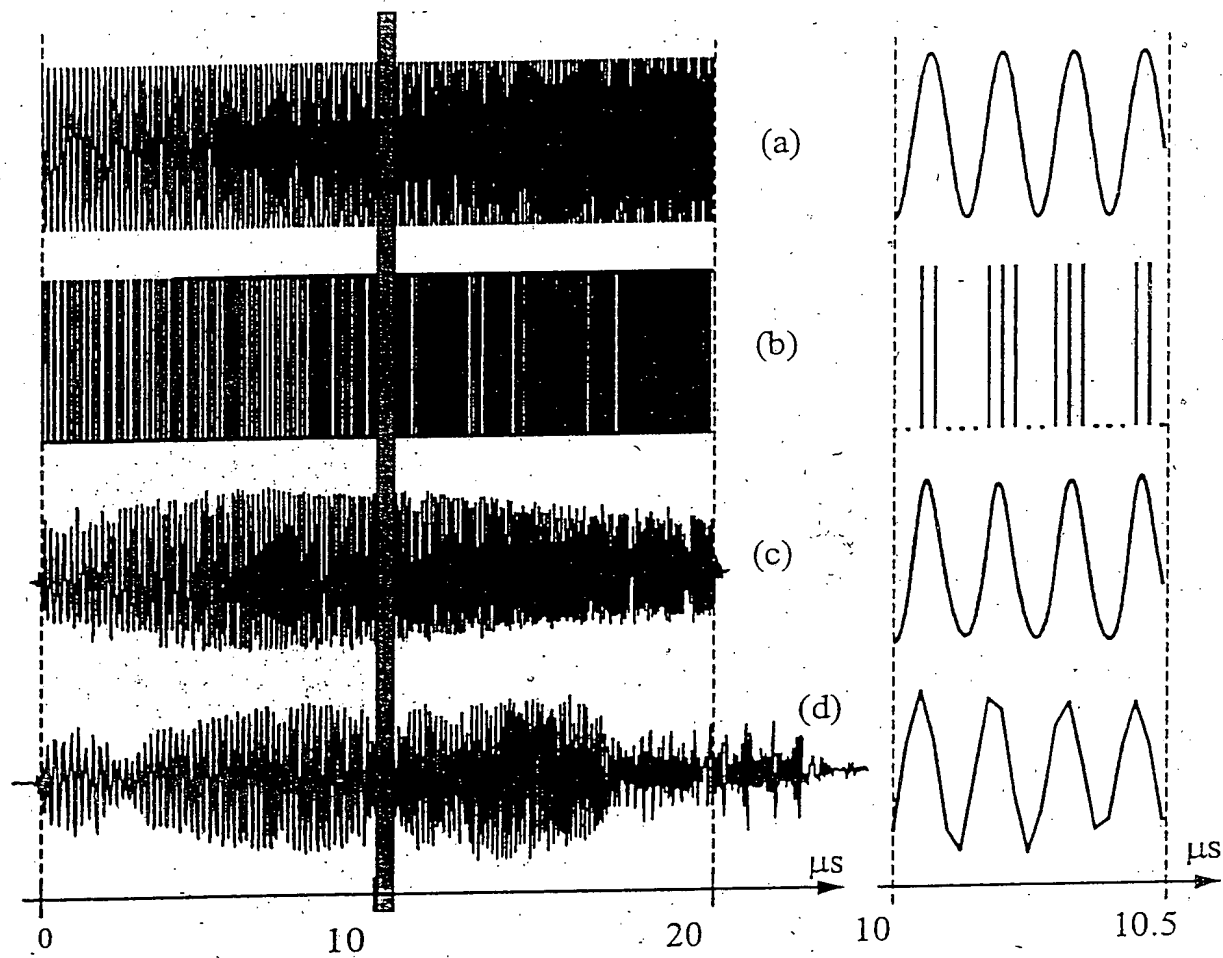


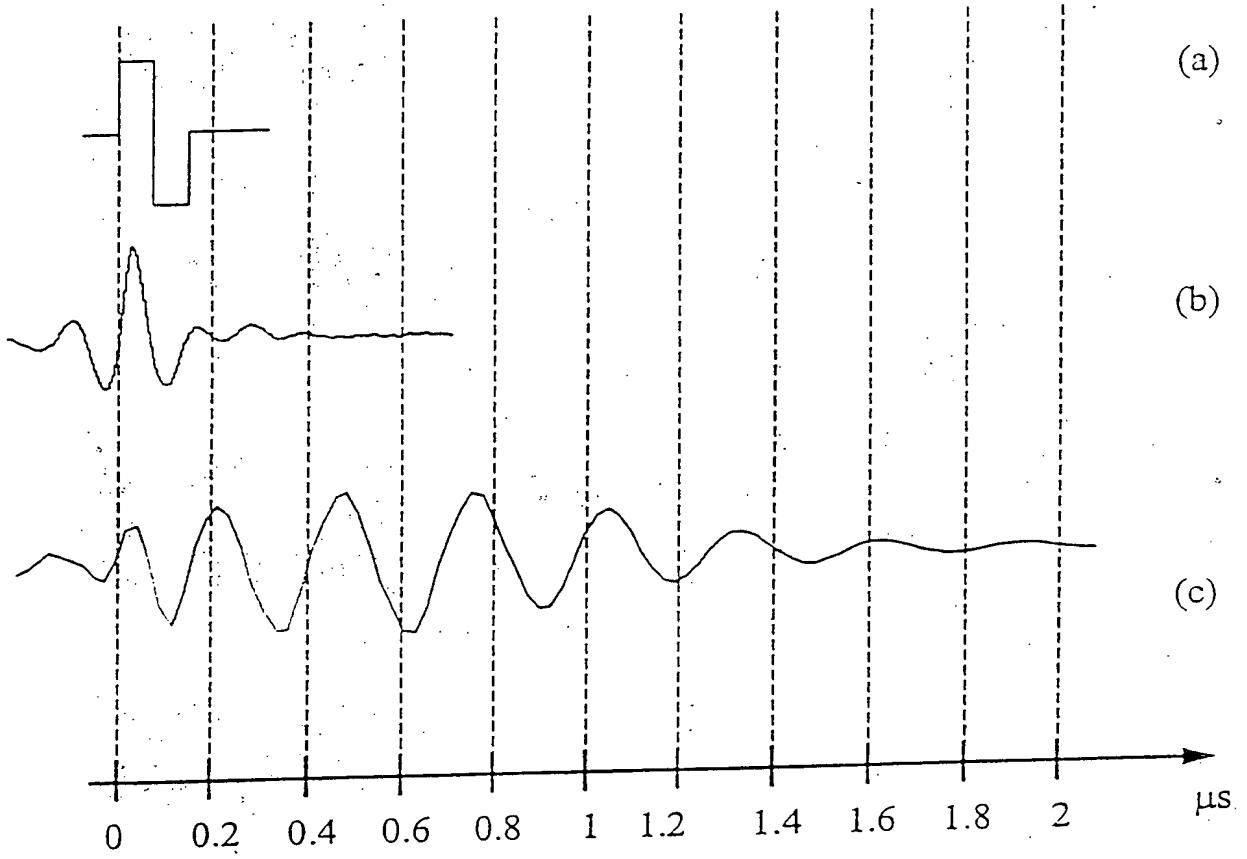
Fig.14





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Fig.15





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Fig.16

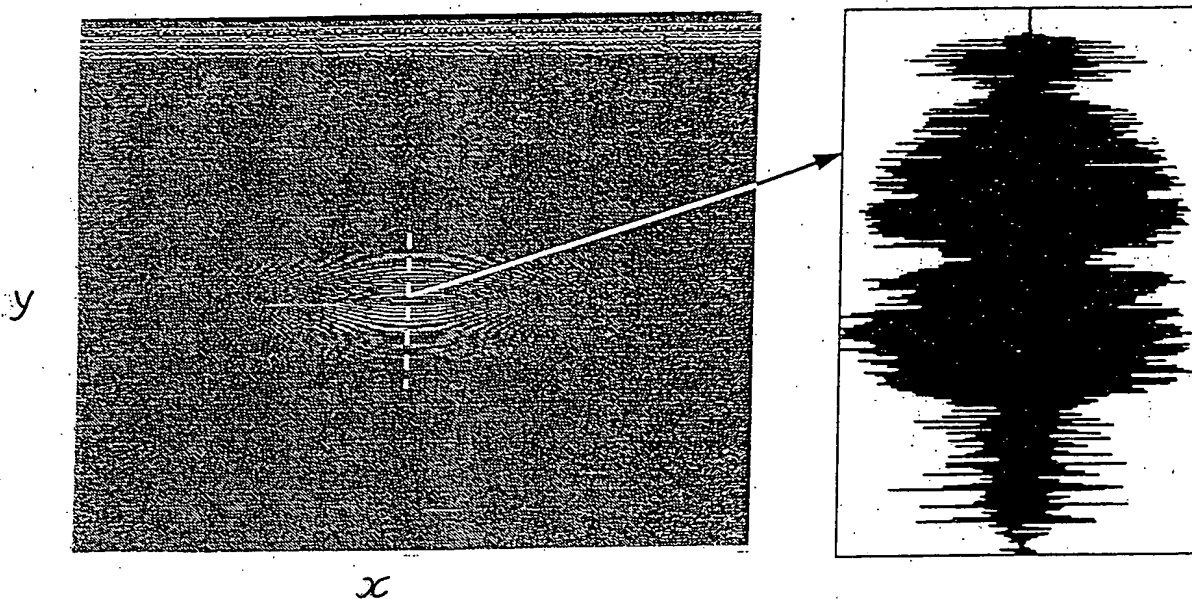


Fig.17

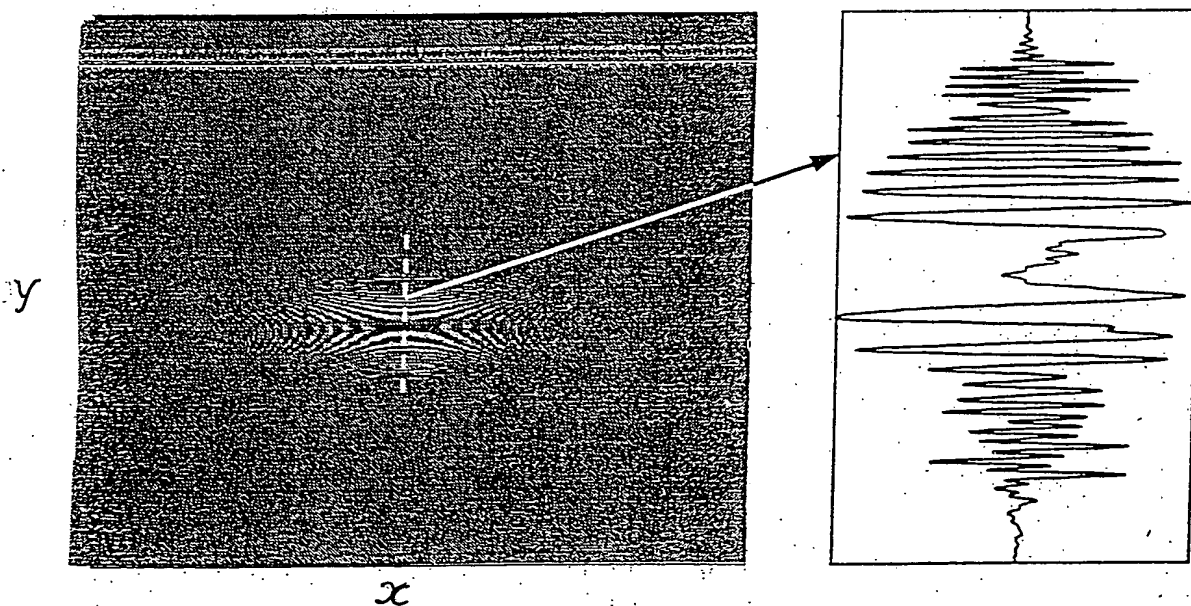
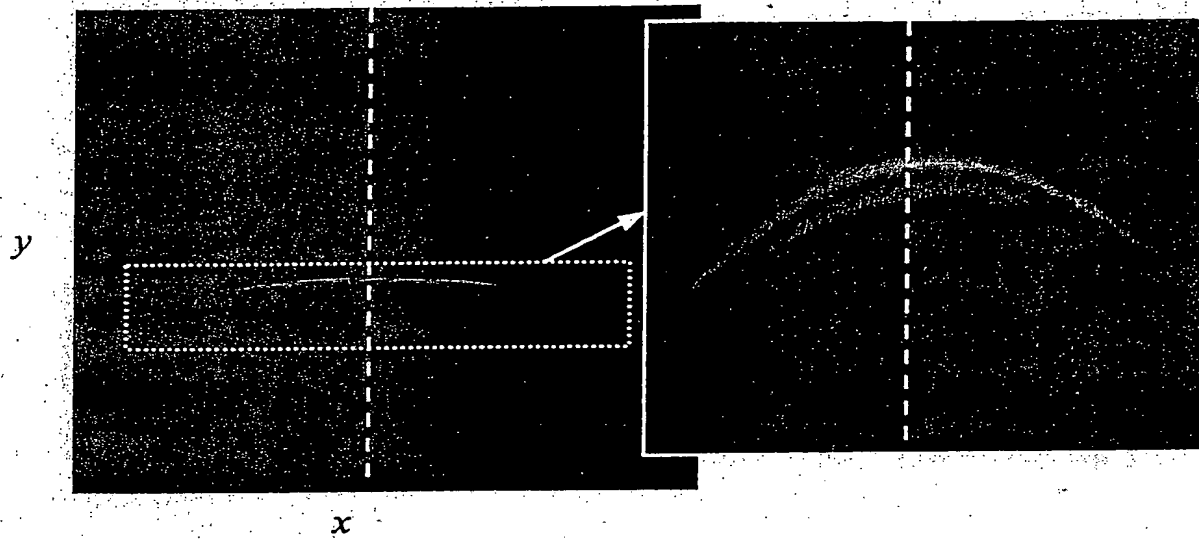


Fig.18

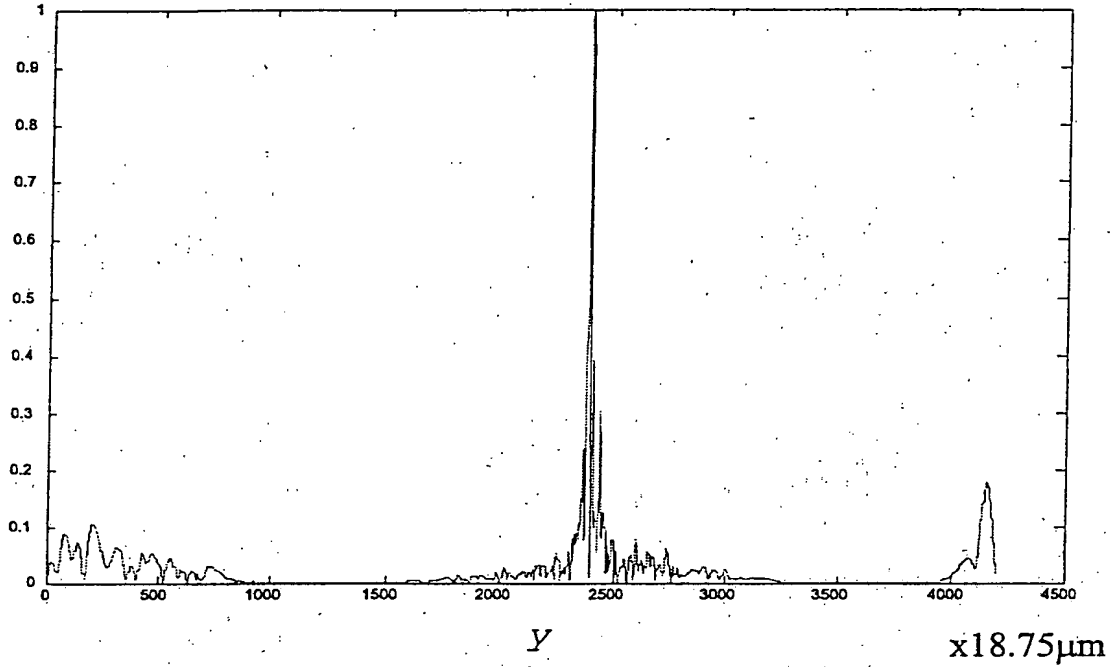




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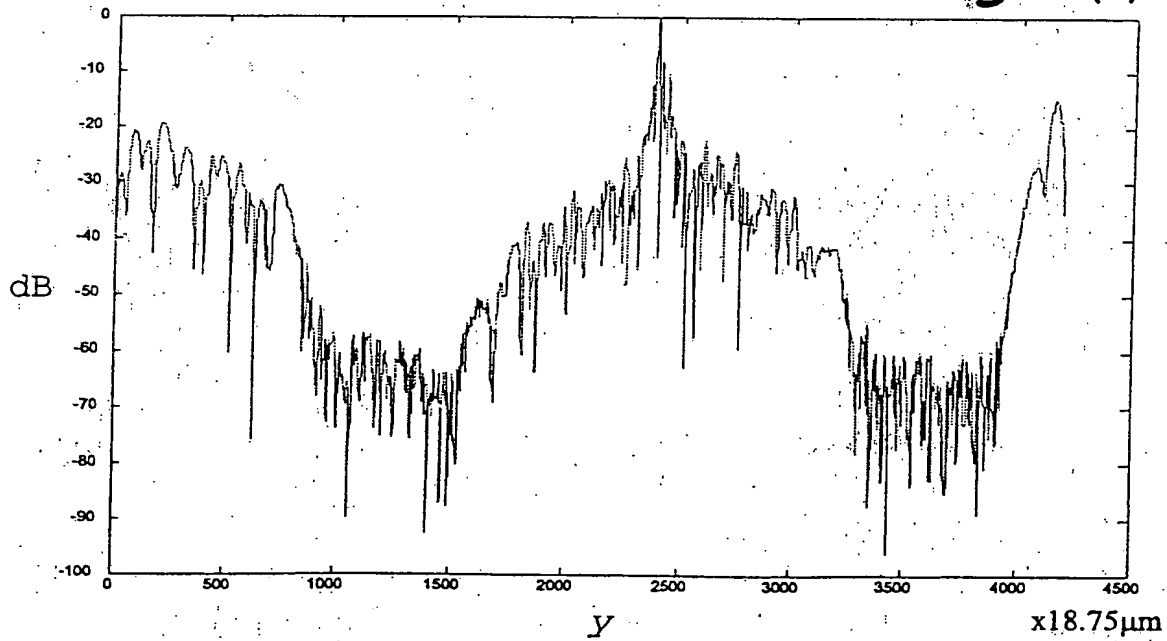
Normalized  
module

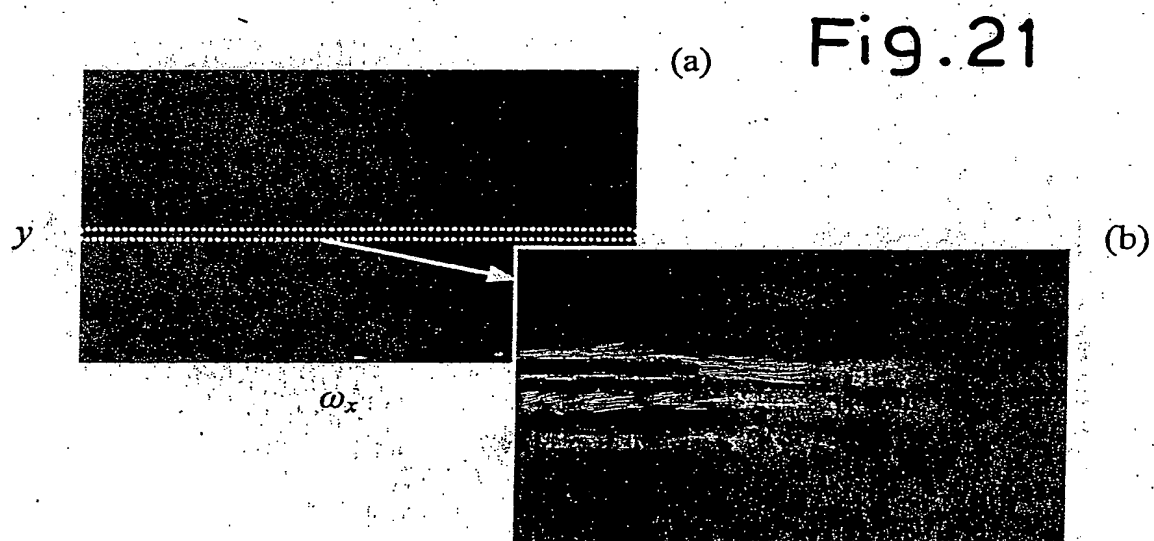
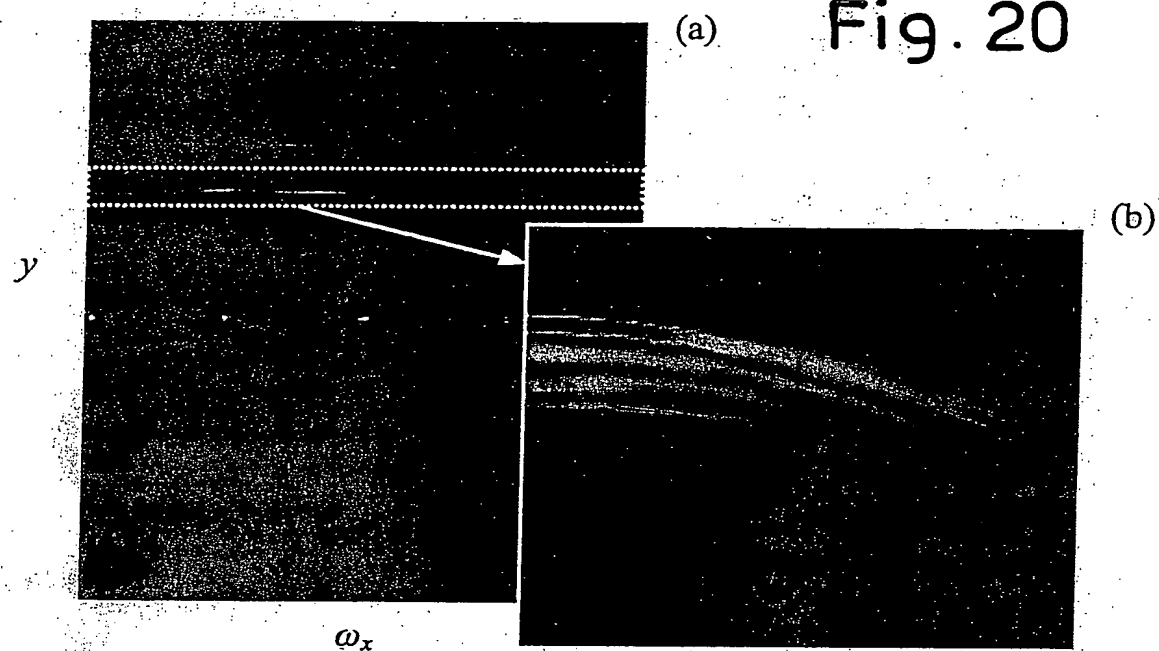
Fig.19(a)



Normalized  
module

Fig.19(b)







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Fig.22(a)

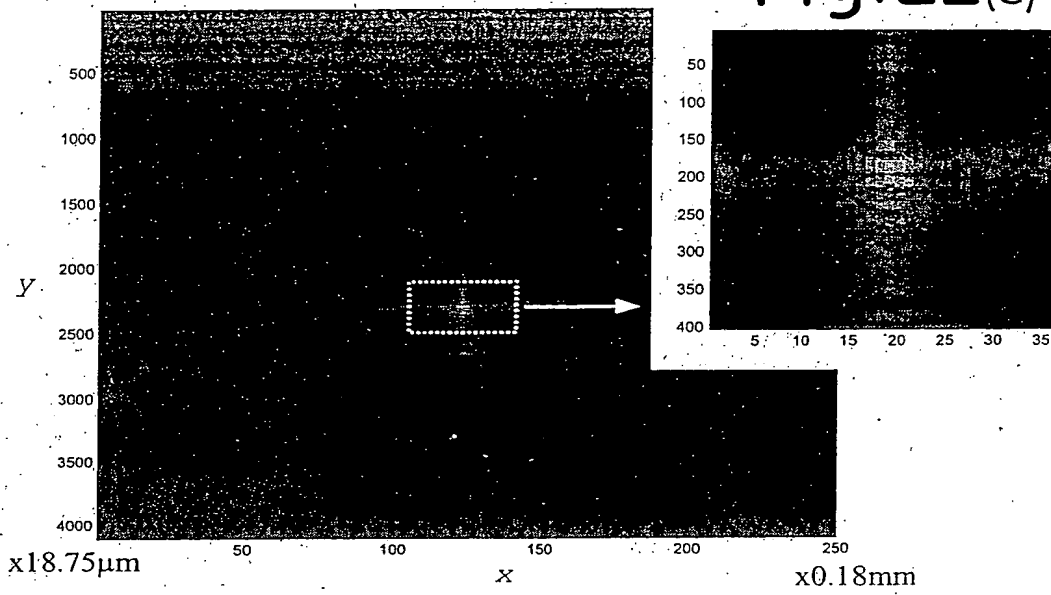
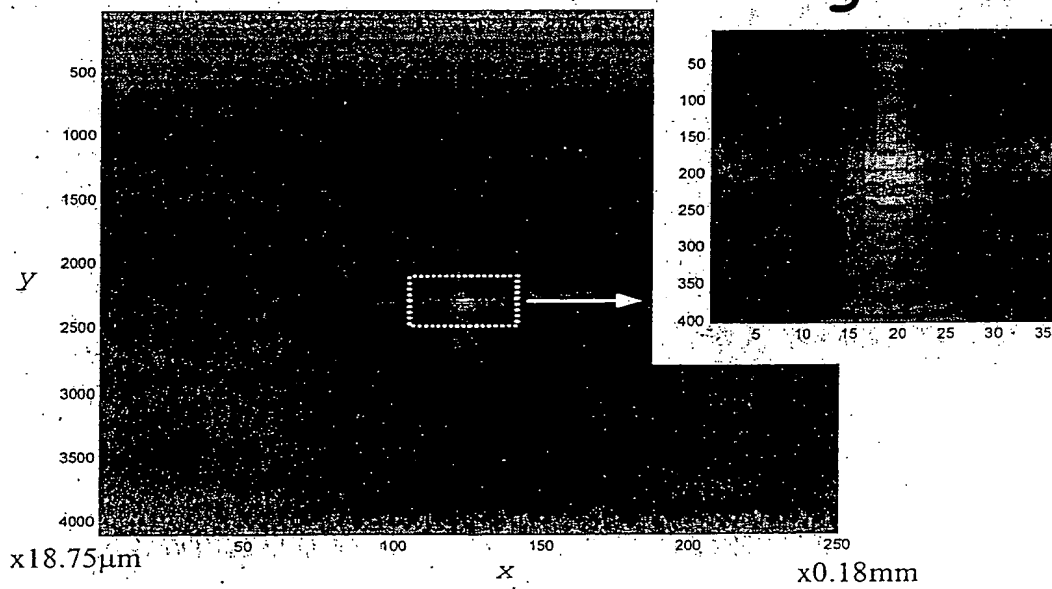


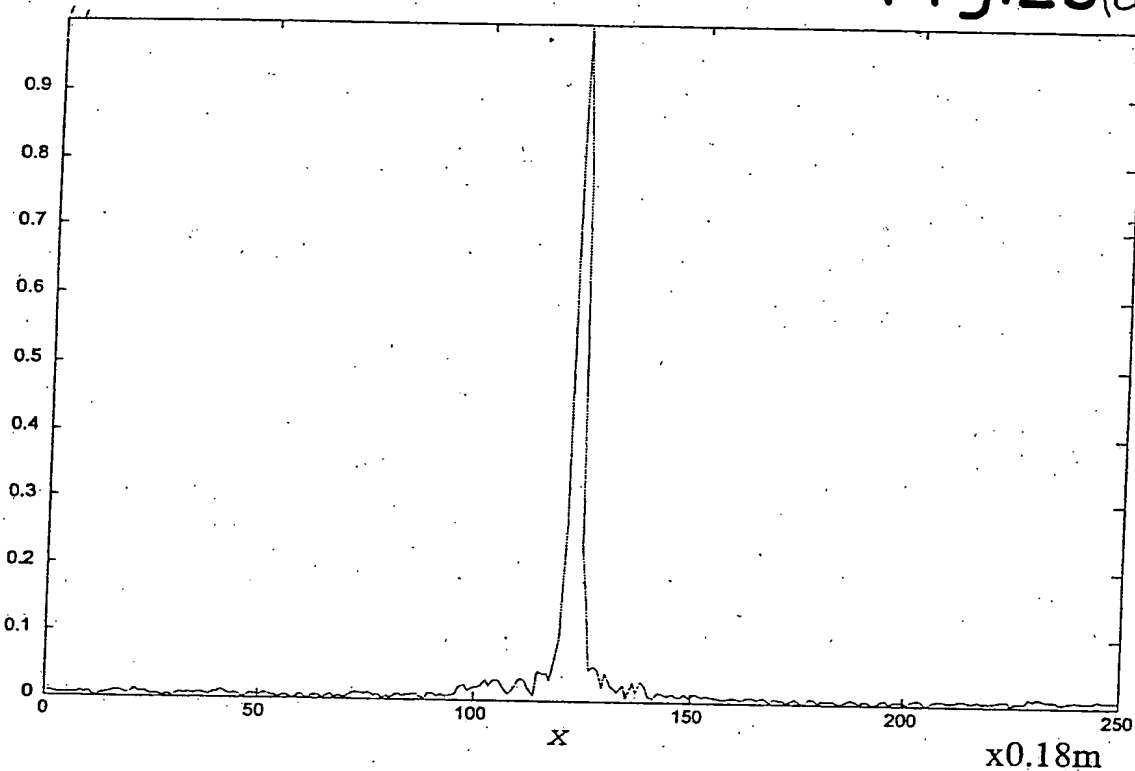
Fig.22(b)





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Fig.23(a)



Normalized module

Fig.23(b)

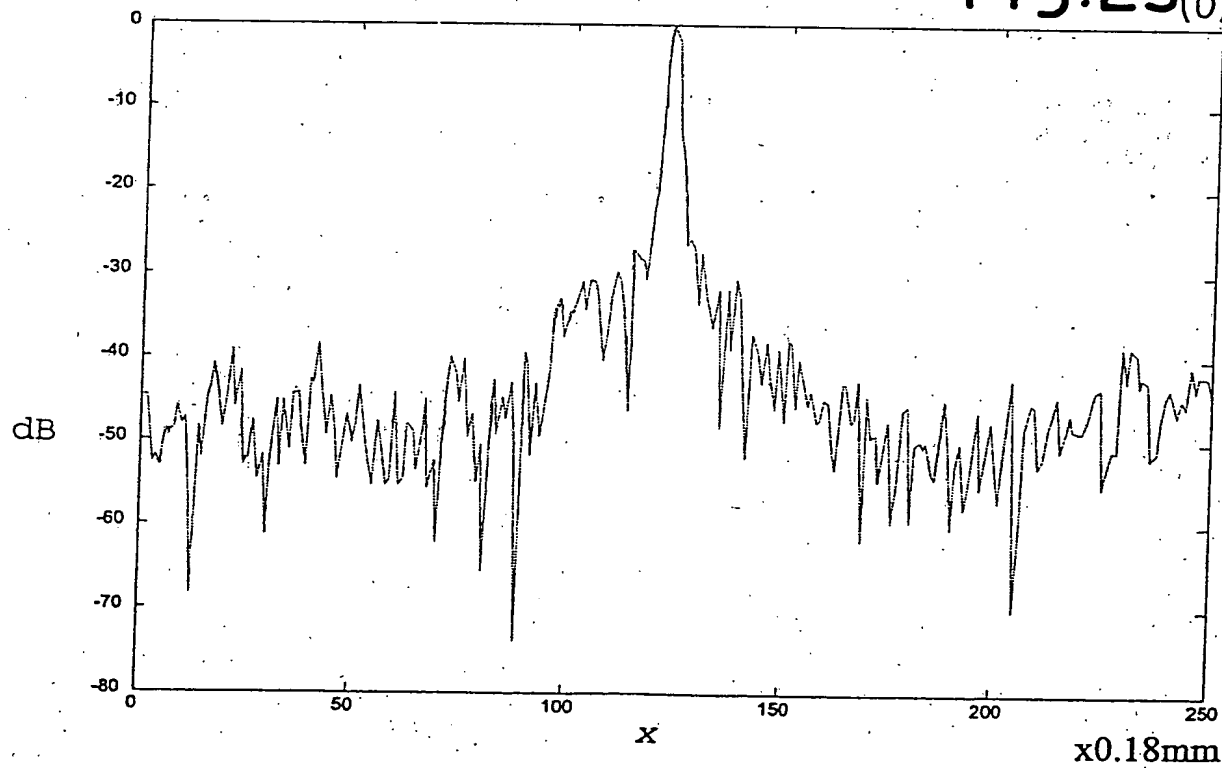


Fig. 24

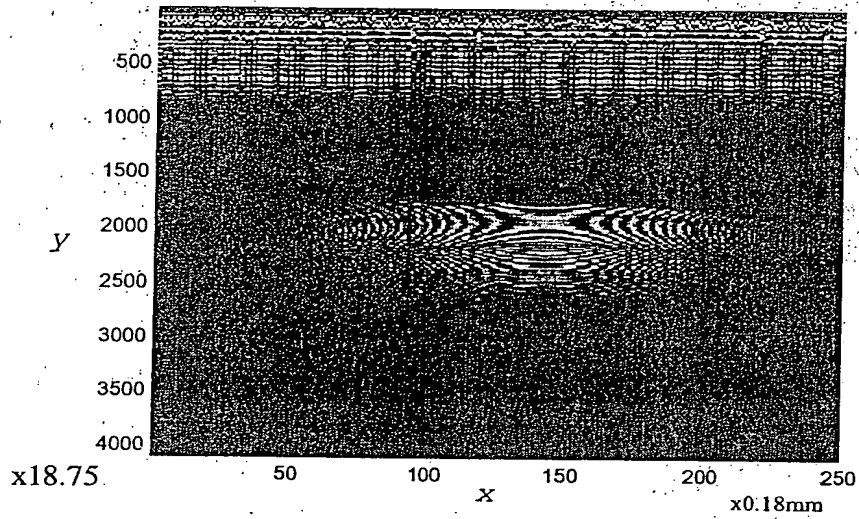
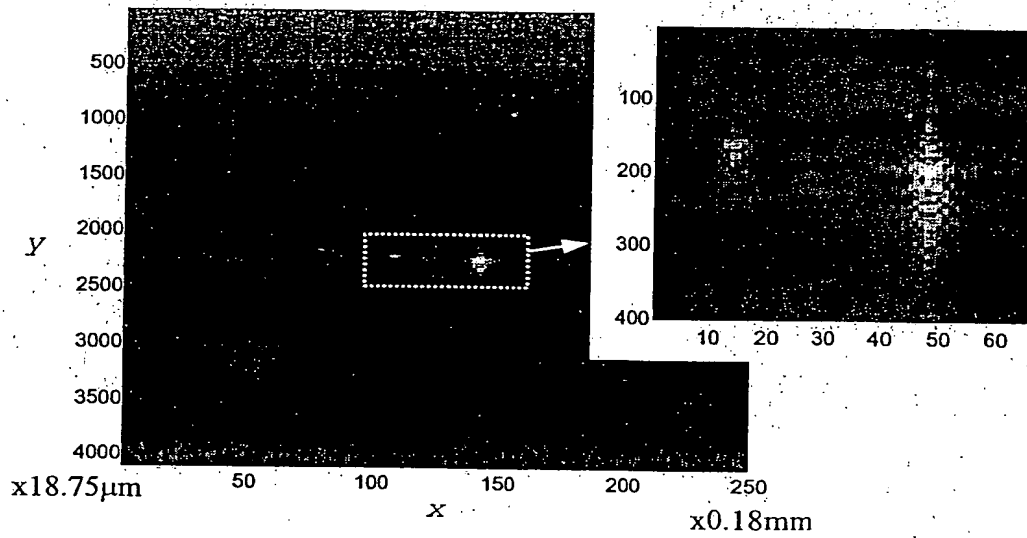


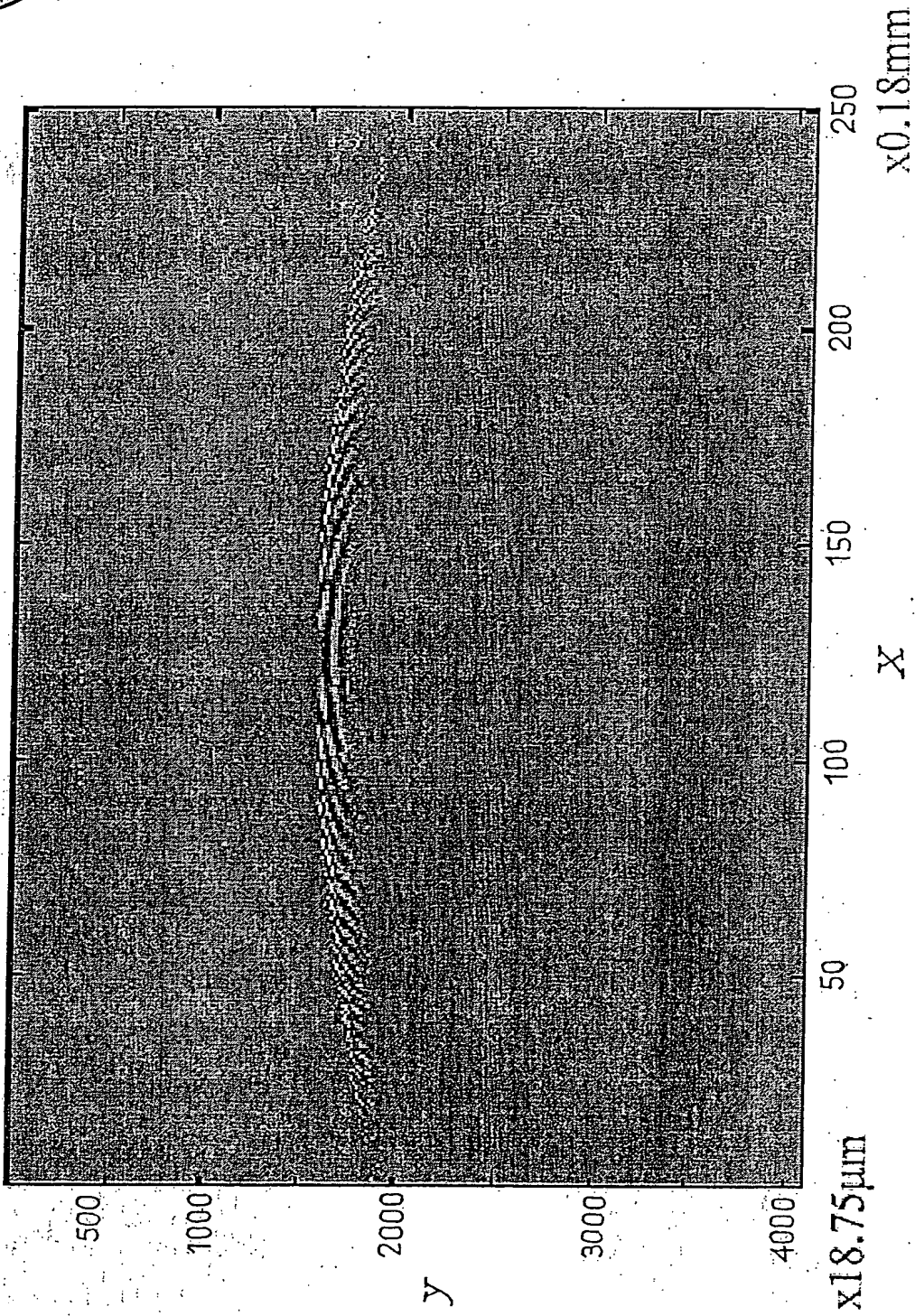
Fig 25





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Fig.26





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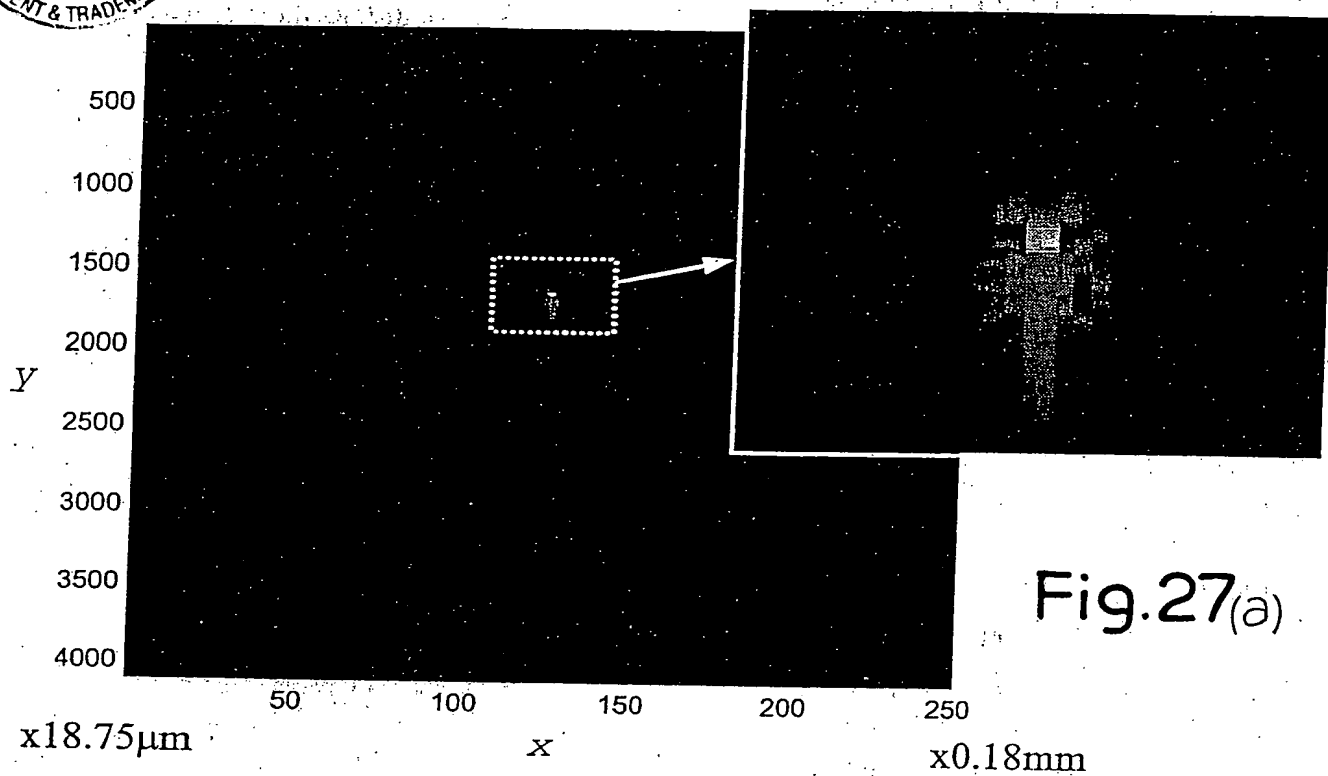
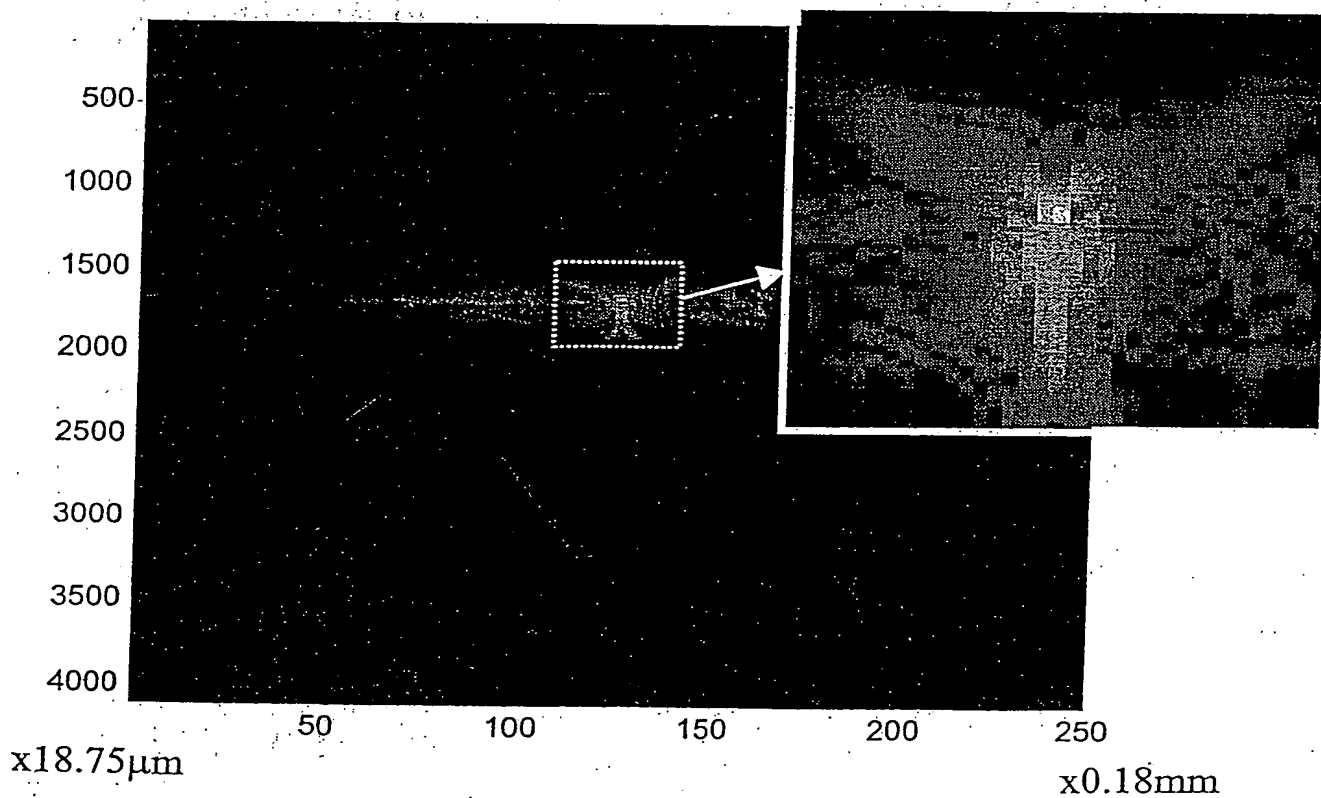
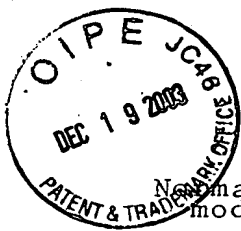


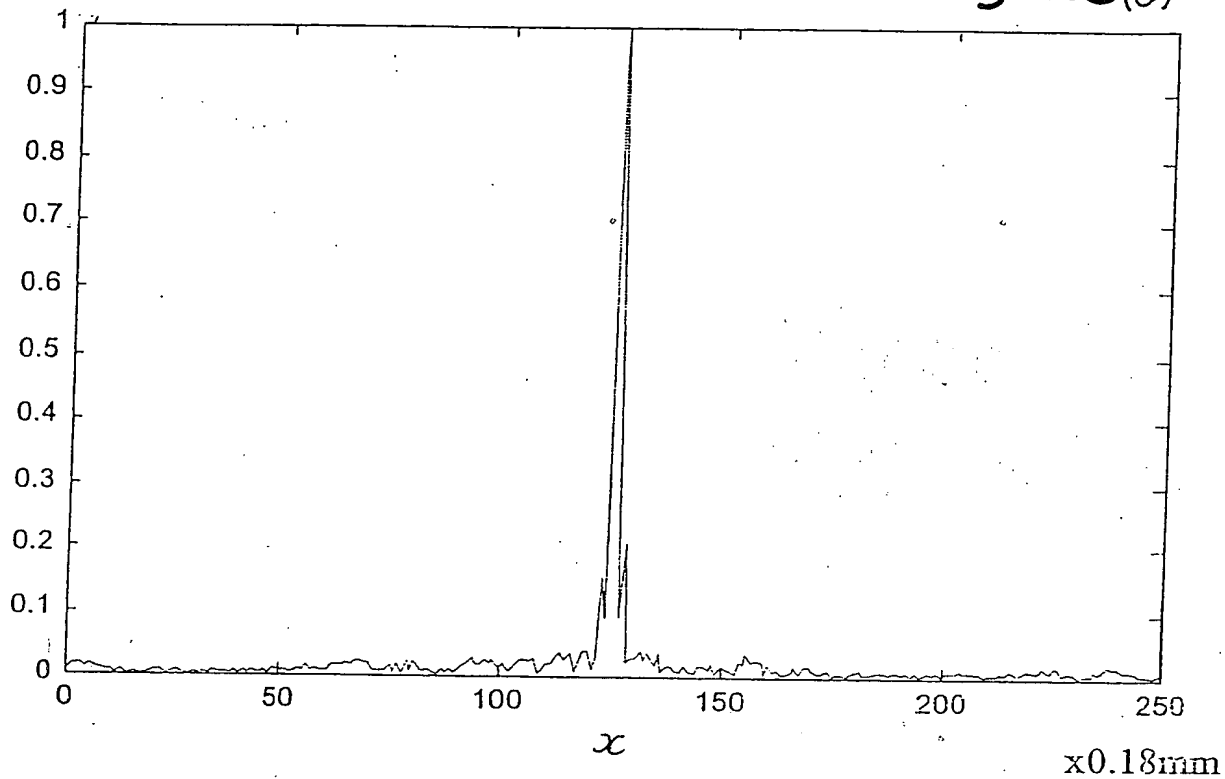
Fig.27(b)





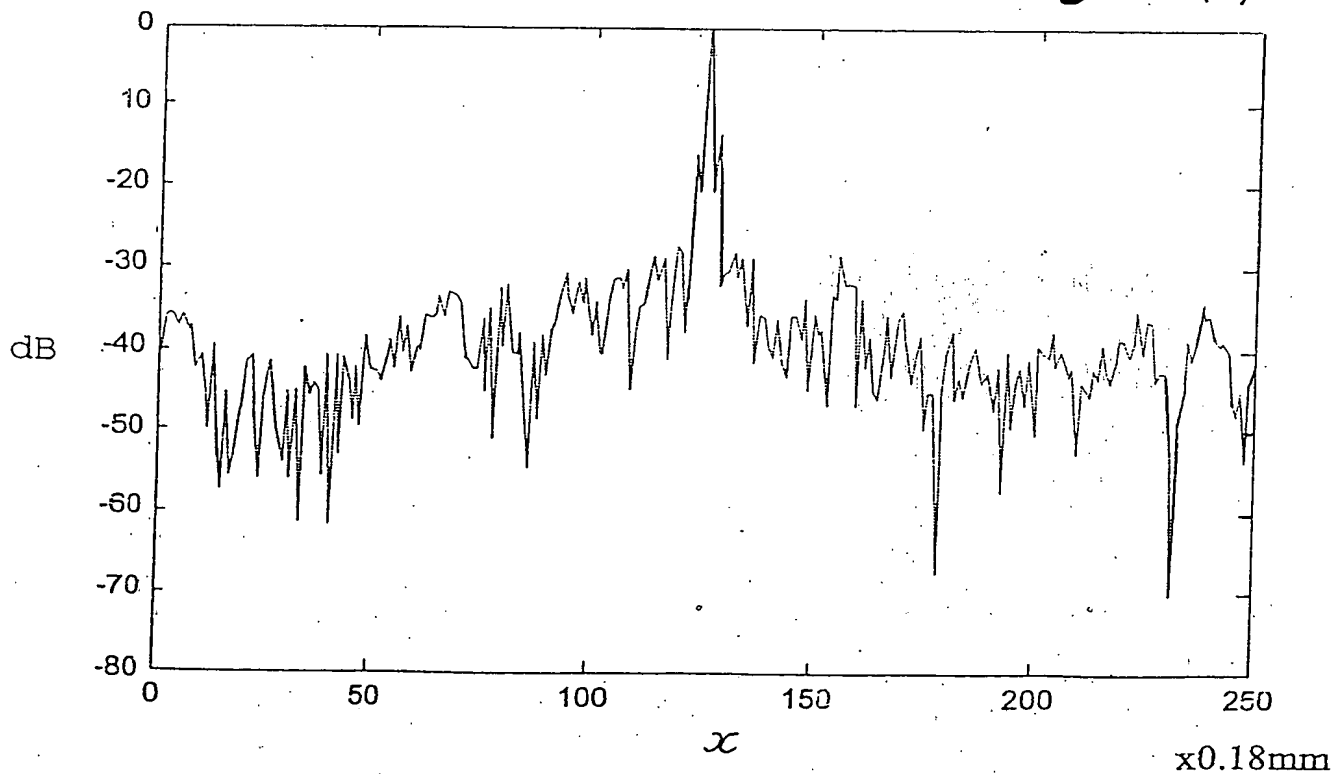
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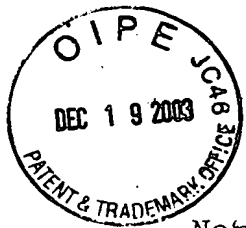
Fig.28(a)



Normalized module

Fig.28(b)

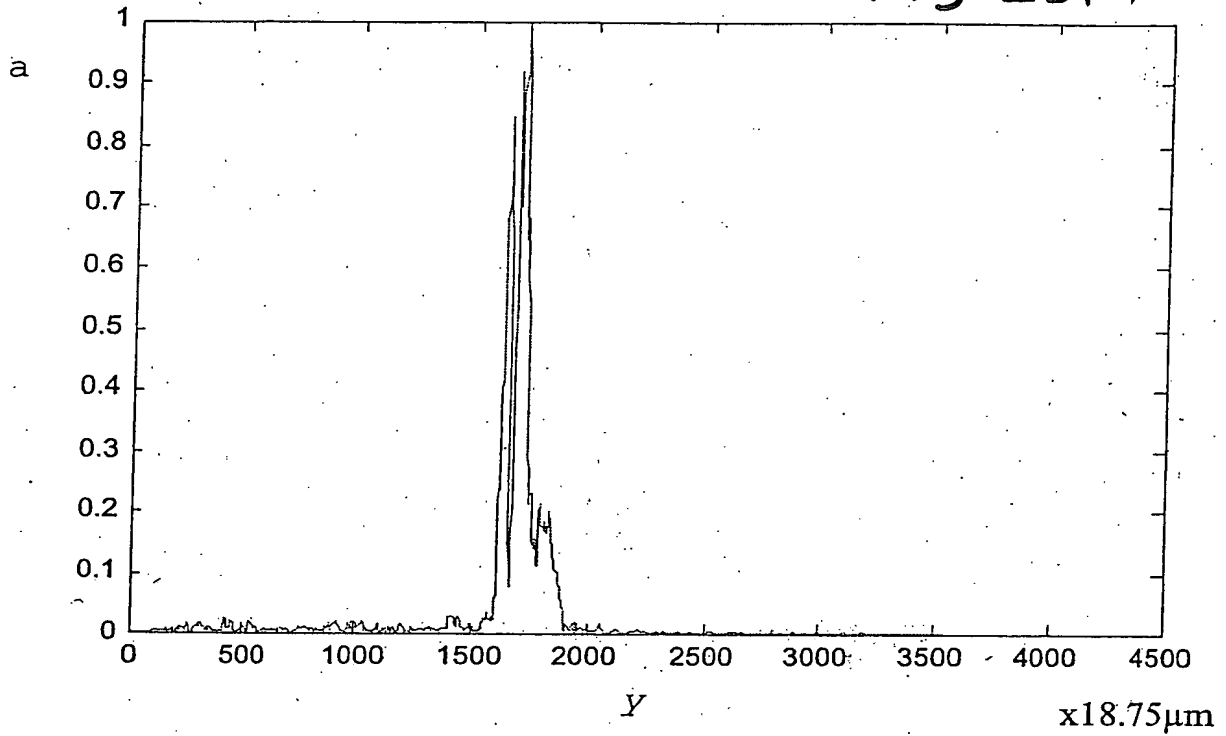




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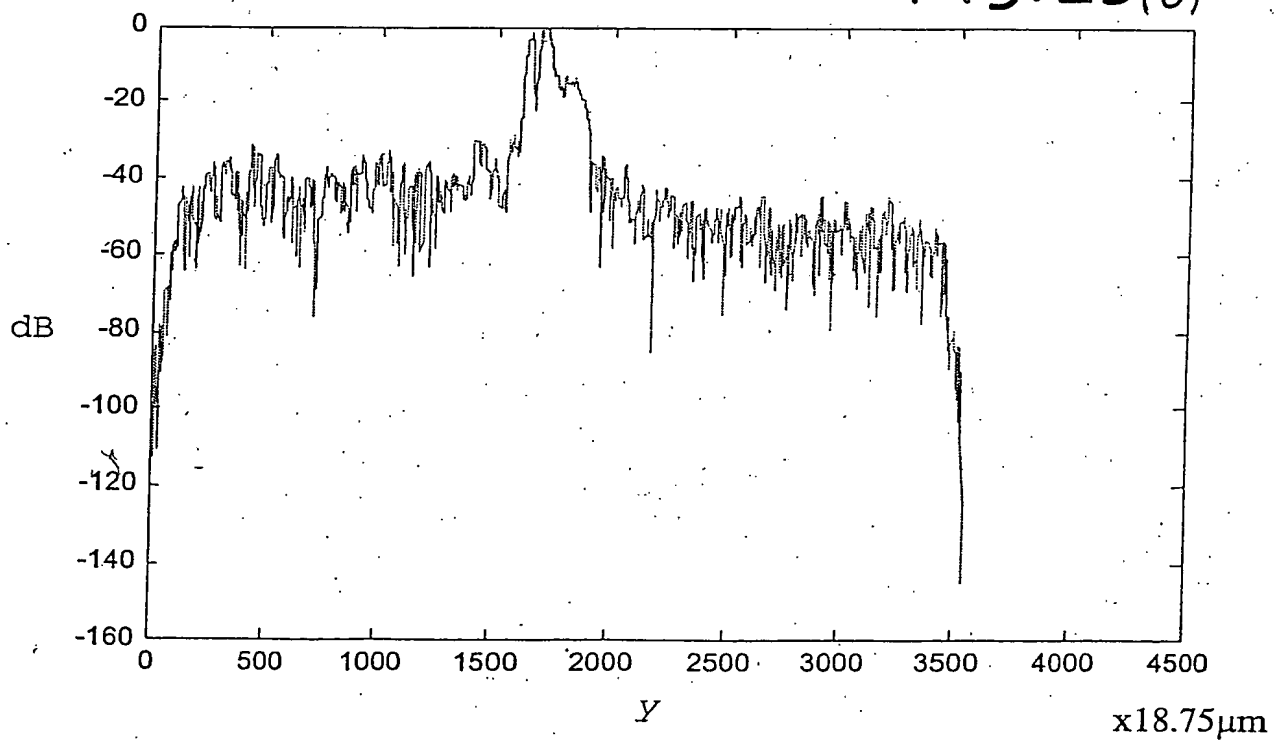
Normalized  
module

Fig.29(a)



Normalized  
module

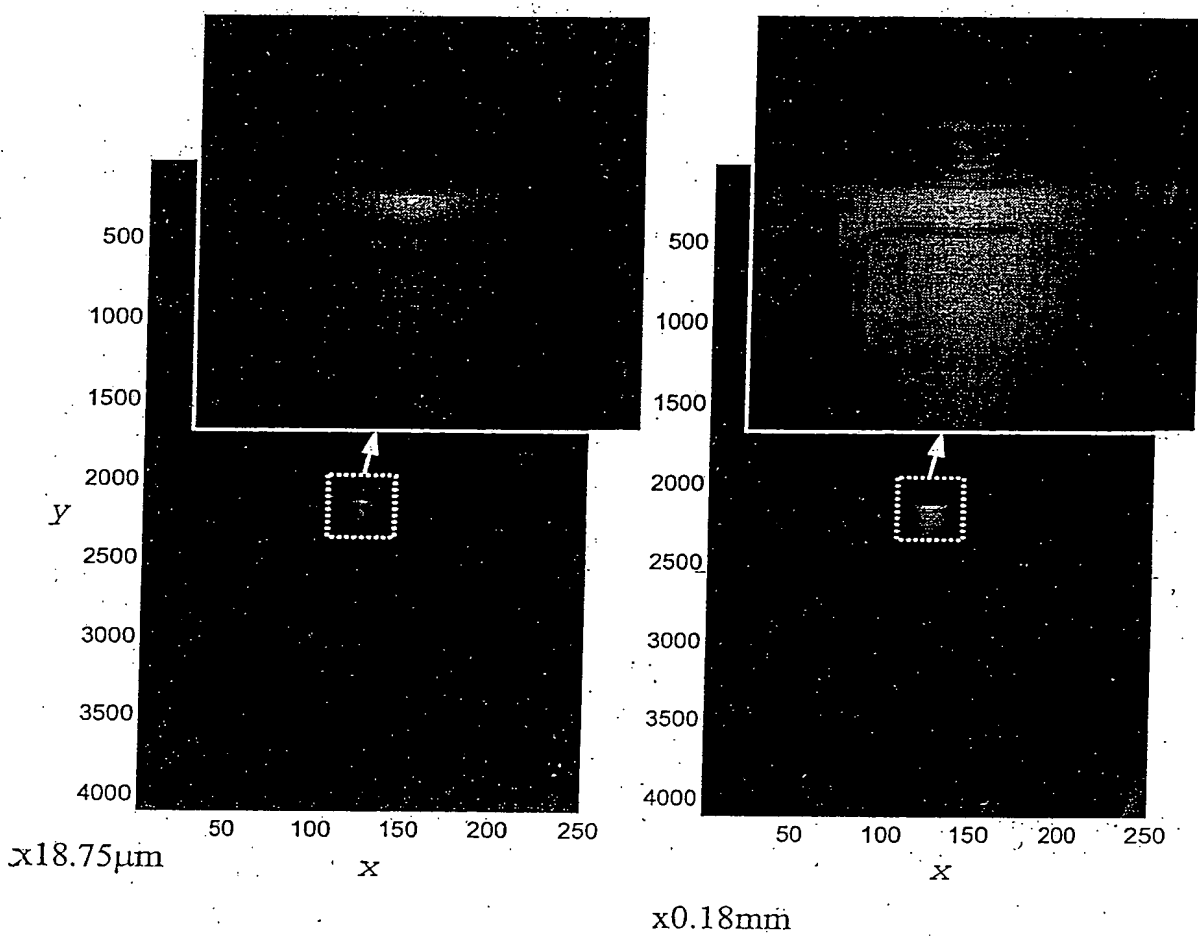
Fig.29(b)





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Fig. 30





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Fig. 31(a)

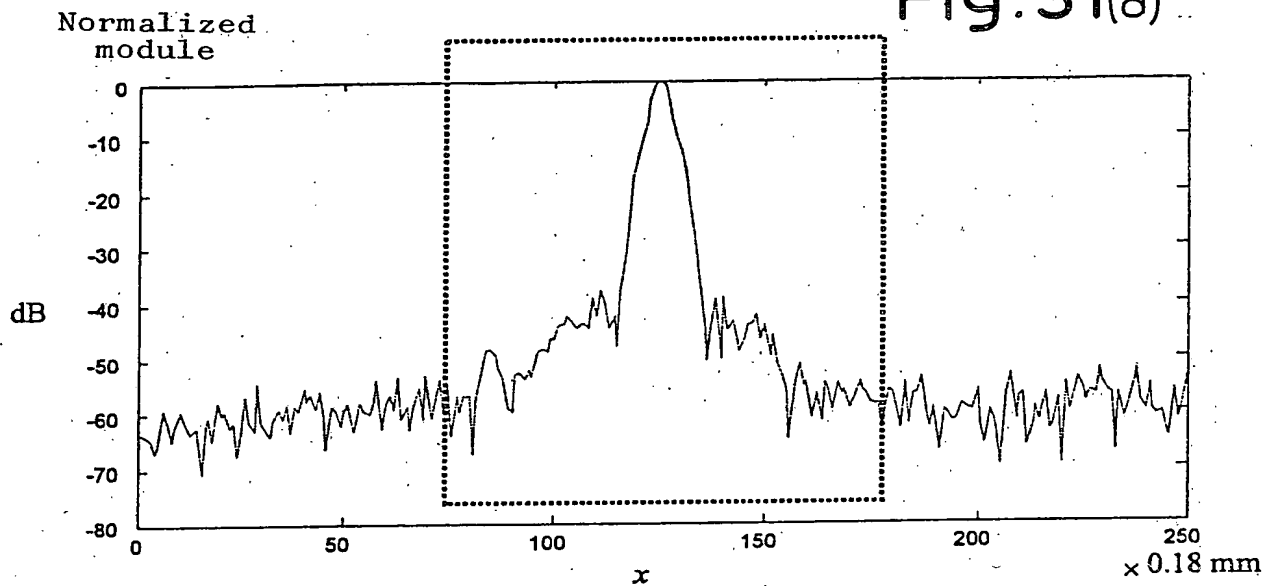
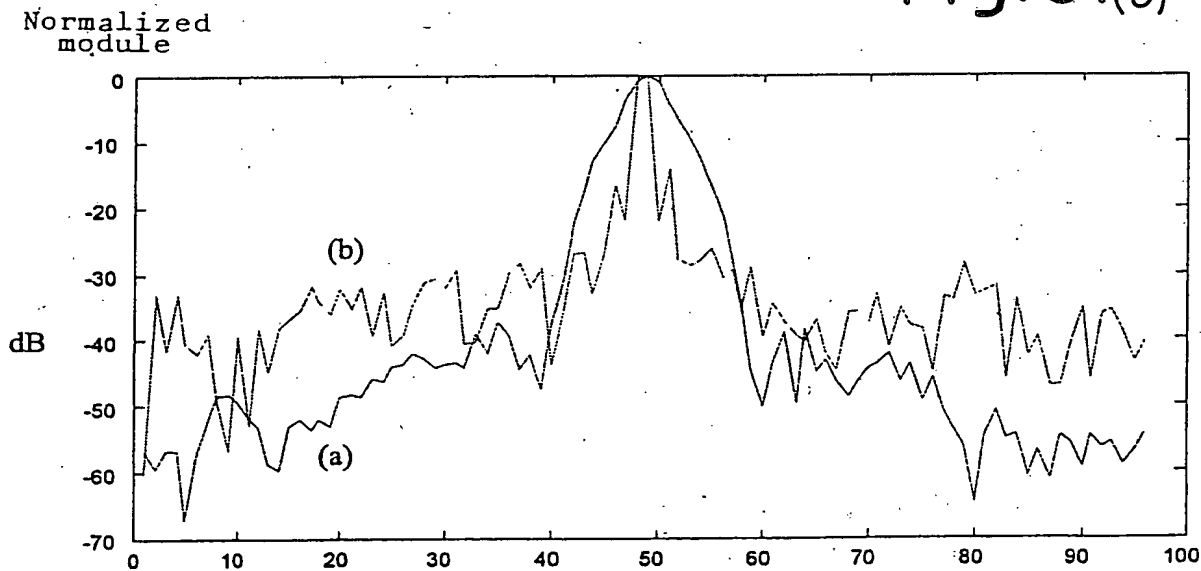


Fig. 31(b)





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Fig.31(c)

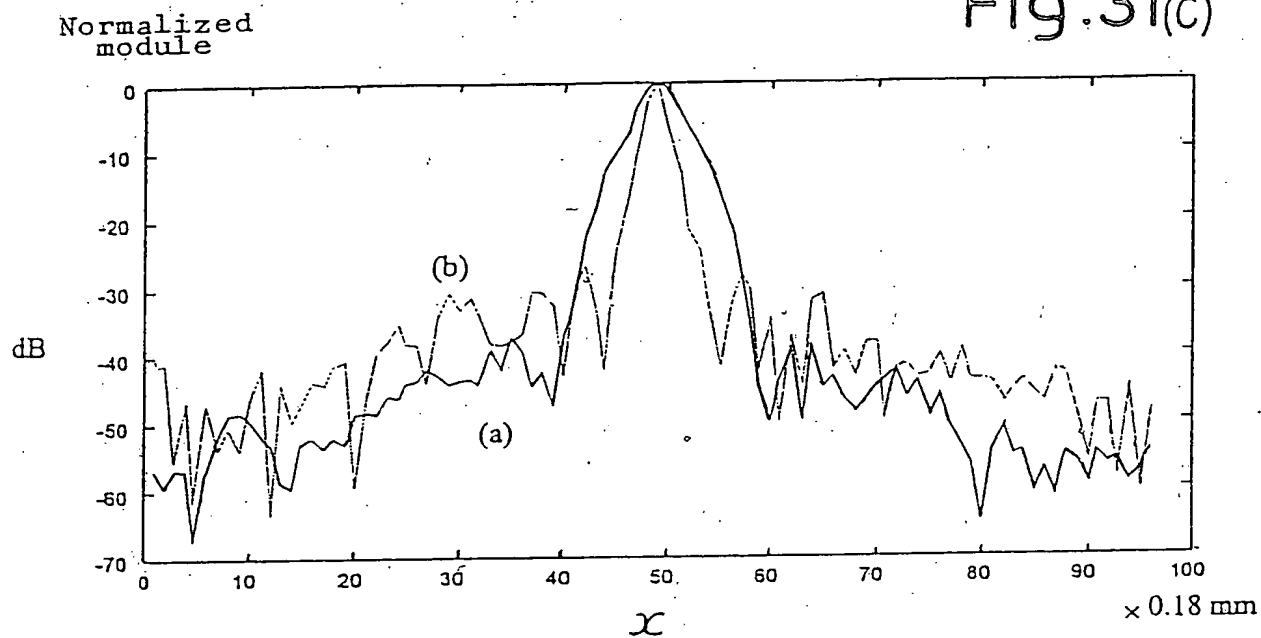
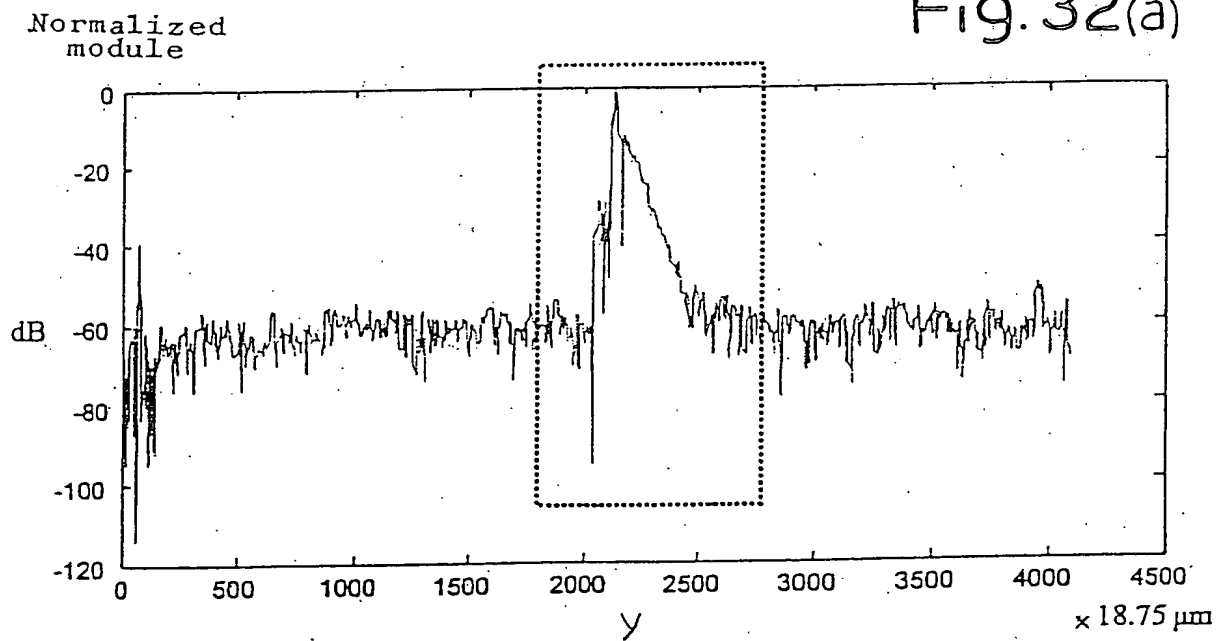


Fig.32(a)





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Fig. 32(b)

Normalized  
module

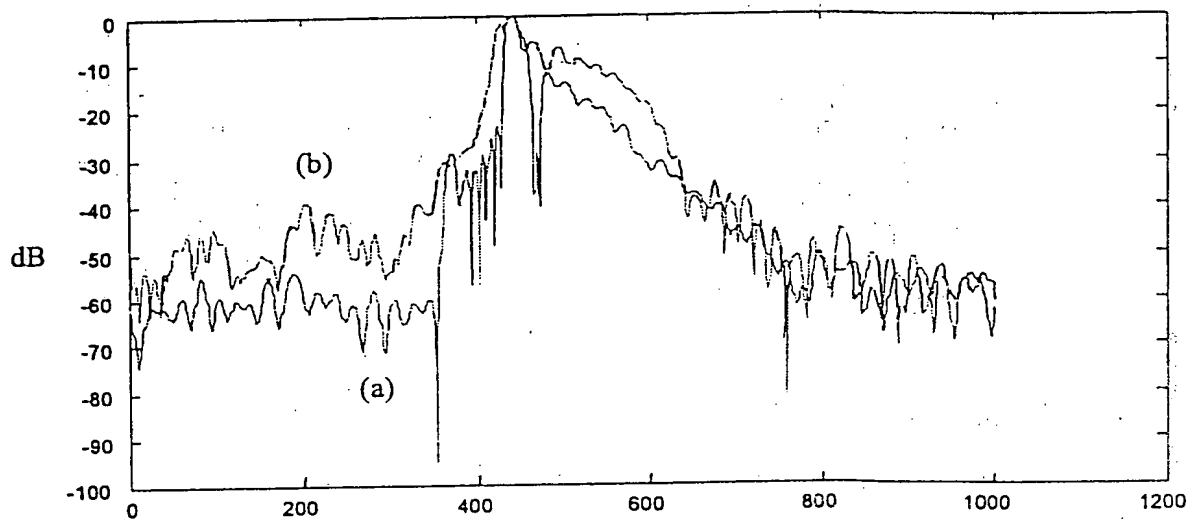


Fig. 32(c)

Normalized  
module

